In the Claims:

Claim 8 has been amended as follows. Claim 9 has been cancelled.

8.   A method for emulating features of an EEPROM memory device incorporated into a memory macrocell which is embedded into an integrated circuit that also includes a microcontroller and a Flash memory structure formed by a predetermined number of sectors, comprising using at least two sectors of the Flash memory structure to emulate EEPROM byte alterability by dividing each of said at least two sectors into a pre-determined number of blocks of the same size and each block into a pre-determined number of pages and updating the emulated EEPROM memory portion programming different memory locations in a single bit mode, wherein at a page update selected page data are moved to a next free block and, when an EEPROM sector is full, all the pages are swapped to another EEPROM sector.

9.   (Cancelled)

REMARKS

At the outset, the Applicant entity (hereinafter "Applicant") would like to complement Examiner Sharon (hereinafter "Examiner") on his Office Action. Specifically, in his Office Action, Examiner set forth the specific recitations of certain of Applicant's claims, and set forth art which Examiner related to such recitations. Although this is the appropriate way in which claims are to be examined, many times such is not done. Applicant thanks Examiner for taking the time to examine the application in such a professional manner.

Status of Claims

Claims 1-20 are pending upon entry of the present amendment. Claims 1-20 stand rejected.